

# Recent improvements in the integration of field emitters into scanning probe microscopy sensors

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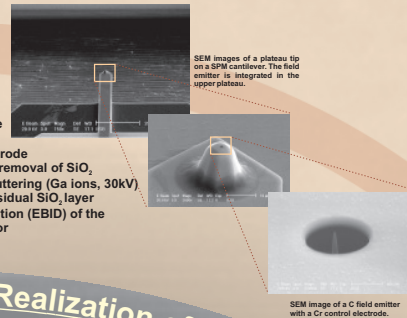
## Motivation

"Improvement of maskless fabricated field emitters in scanning probe microscopy (SPM) sensors with respect to process yield, process stability and compatibility to semiconductor processes"

- Strategies and processes are presented and discussed which enable the integration of electron beam induced deposited (EBID) emitters into scanning probe microscopy (SPM) sensors.
- The change from a Pt to C emitter precursor material led to improved process stability and electrical performance.
- A Cr control electrode instead of a Pt control electrode improves compatibility to standard silicon semiconductor technology.
- A 30nm thick oxygen rich layer between the emitter and the Si-substrate has a great influence on the turn-on voltage of the field emitter. This was eliminated by advanced processing.
- Further work has to be done in the electrical characterization of the C field emitters with intermediate silicide layer.
- Results of this work significantly ease the use of such field emitter probes for real applications.

## First approach:

- highly doped Silicon substrate
- 1µm insulating SiO<sub>2</sub> layer
- 0.2µm Pt layer as control electrode
- electrode opening and partial removal of SiO<sub>2</sub> by focused ion beam (FIB) sputtering (Ga ions, 30kV)
- wet etching (HF) to remove residual SiO<sub>2</sub> layer
- electron beam induced deposition (EBID) of the emitter with platinum precursor



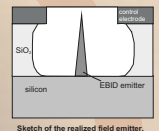
## Improved approach:

Change of the control electrode material from Pt to Cr:

- better compatibility to semiconductor technology

Change of the emitter material from Pt to C:

- suppressed overspray (with Pt precursor, Pt traces at the SiO<sub>2</sub> walls lead to conductive paths between emitter and gate)
- higher stability against bending during emission



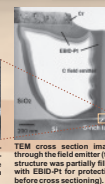
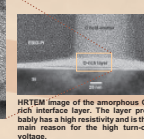
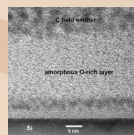
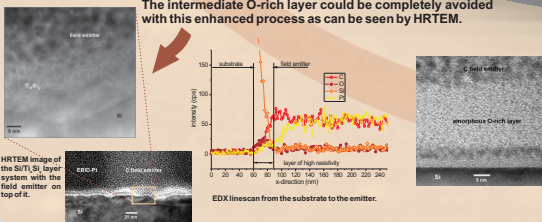
## Conclusions

## Realization of Field Emitter

First results clearly identify a 30nm thick intermediate layer between the substrate and the emitter, which contains high amounts of oxygen as shown by EDX. Thus, the layer has a high resistivity and is assumed to be the main reason for the high turn-on voltages.

To avoid this oxygen rich layer, a conductive layer between the emitter and the substrate material can be deposited which prevents the oxidation of the silicon surface. Therefore, a Ti silicide has been formed, with the emitter deposited on top of it.

The intermediate O-rich layer could be completely avoided with this enhanced process as can be seen by HRTEM.

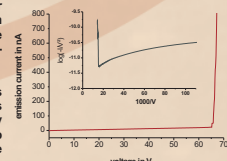


TEM, HRTEM and EDX analyses

## Results and Discussion

Attempt to decrease the turn-on voltage by reducing the diameter of the control electrode from 1µm to 500nm → no significant change in the turn-on voltage could be obtained.

As for such electrode diameters and similar emitter geometries published values are significantly lower (e.g. 30-40V for Pt tip emitters), a high resistance in the device structure must be the reason for the high turn-on voltage.



## Electrical Characterization:

- sensor bonded on a TO-5 type mount
- placed into a vacuum chamber (10<sup>-6</sup> mbar)
- measurement of the IV-curve: voltage between emitter and control electrode is ramped up until emission starts

- emission current is detected between emitter and a target grid facing the sensor at a distance of 5mm

## Experimental results:

The Fowler-Nordheim plot reveals a linear dependence, indicating that emission of electrons is caused by field emission.

The turn-on voltage is 65V. This is too high for many possible applications and for a simple integration into scanning probe microscopy systems.

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