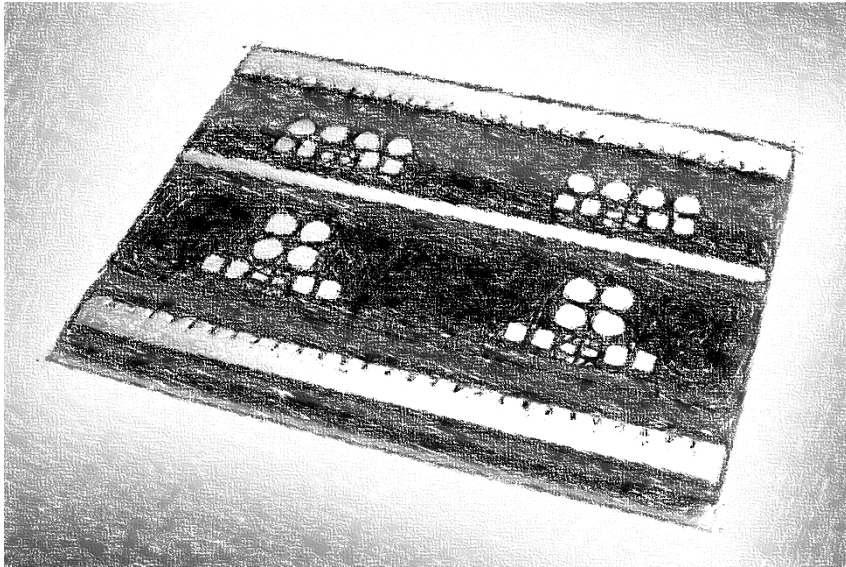
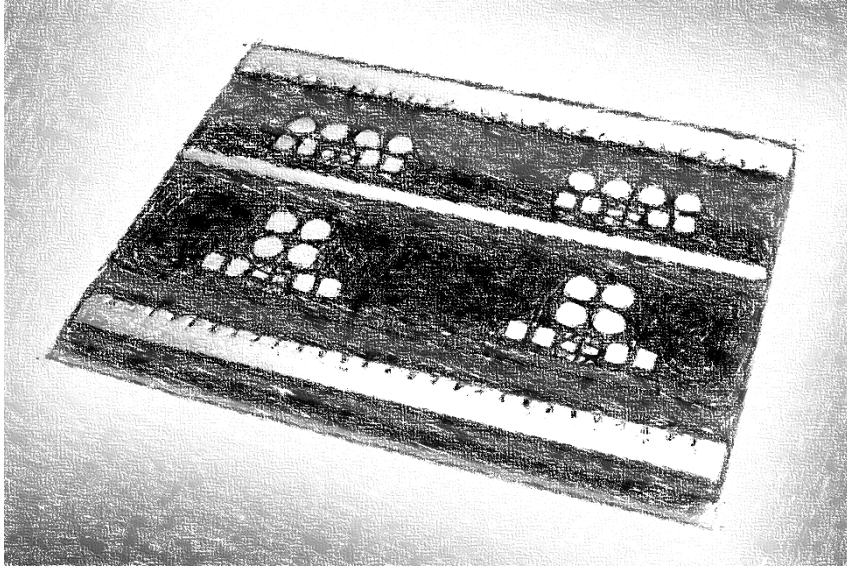

Modular Power Electronics, Realized by PCB Embedding Technology



- Lars Böttcher
- Fraunhofer IZM Berlin
- lars.boettcher@izm.fraunhofer.de

Outline



■ Introduction

- Embedding for Power Semiconductors
- Technology

■ Modularization of Power Modules

■ Applications

- Photovoltaic Power Inverter 2.3 kW up to 125kW
- Electrical bicycle Inverter 50kW
- Automotive Inverter 50 kW

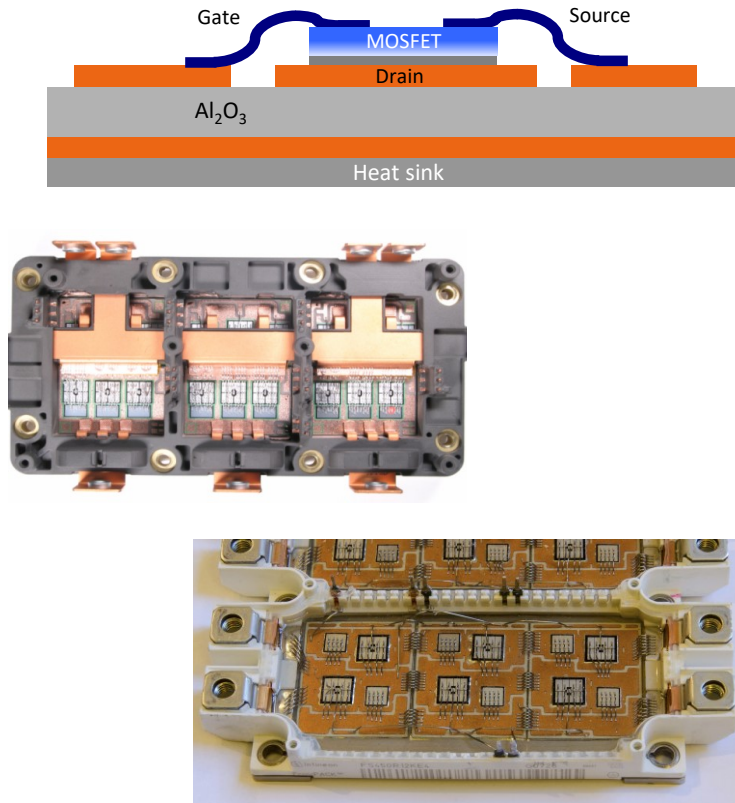
■ Conclusion

■ Acknowledgements



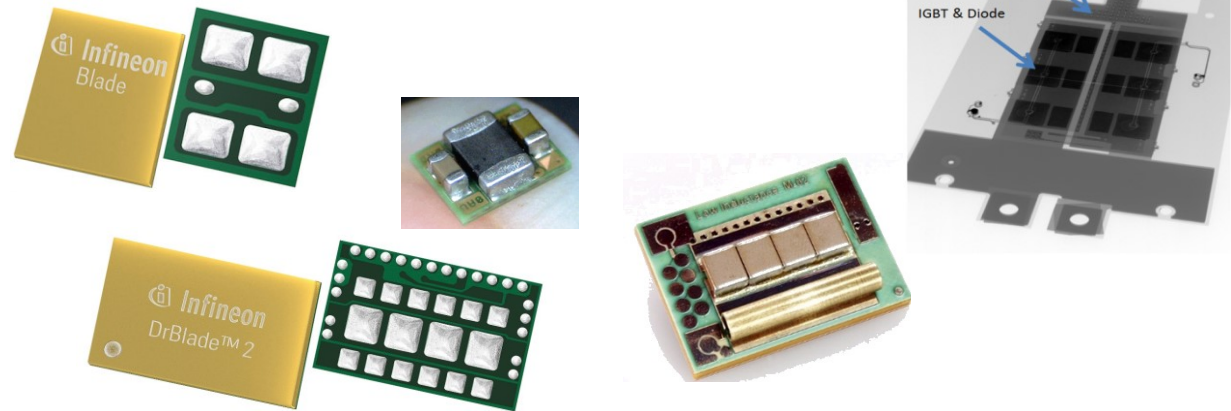
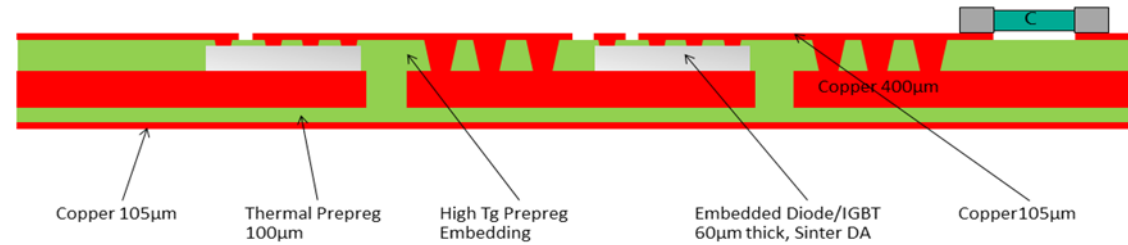
Power Chip Embedding - Opportunities

wire bonding on DCB



- ➔ strong restriction of designs by technological limits
- ➔ Highly inductive connection by wire bonds

embedding



- ➔ Huge design space is open for exploration
- ➔ Improve electrical performance, low inductive connection to die



Power Chip Embedding – Process



conductive die attach



Embedding by vacuum lamination



Micro via drilling



Cu metallization and patterning

- Substrate: leadframe, copper foil, high current PCB
- Conductive die attach: various
- Embedding in FR4 prepreg layer:
 - Structured layers for dies
 - Full layer for top side isolation
 - Cu foil
- Blind via manufacturing:
 - Laser via to embedded die
 - Laser or mechanical drilling to substrate
- Cleaning, Activation and Cu metallization
- Patterning of circuitry

Power Embedding – Features

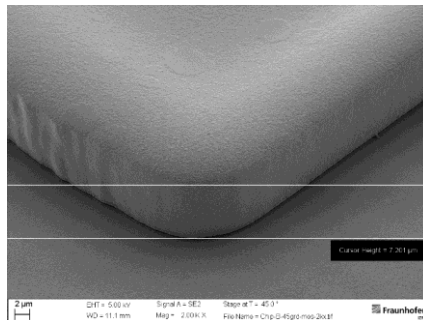
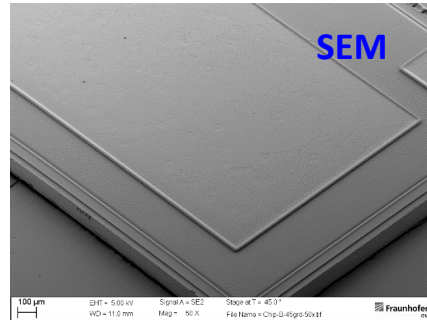
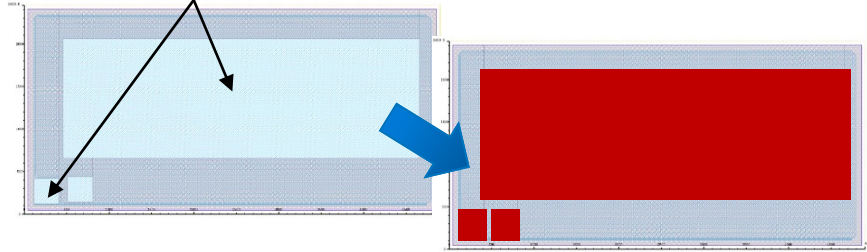
- Embedding of power chips into Printed Circuit Board structures
 - Multiple wiring layers
 - Very low parasitic effects
 - Flat modules → SMD assembly on top is possible
 - Top side cooling possible
 - High reliability by direct Cu to chip interconnects and embedding
 - Capablility for integrated shielding
 - Cost saving by large area process → Panel Level Packaging



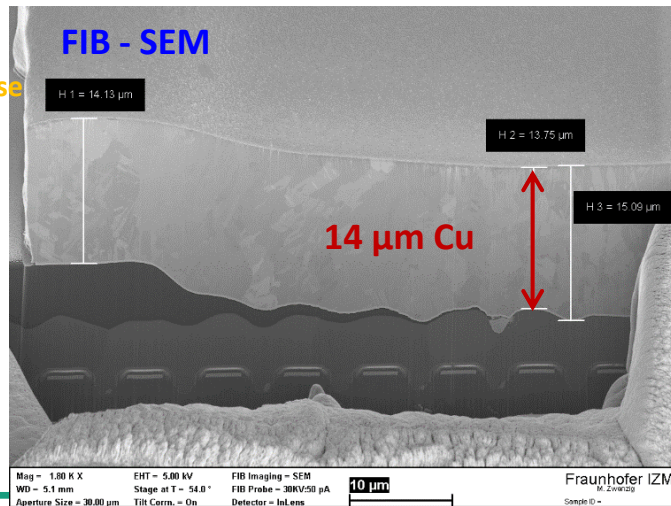
Power Chip Embedding – Special requirement

Contact Pads Al ~ 1 µm)

Good

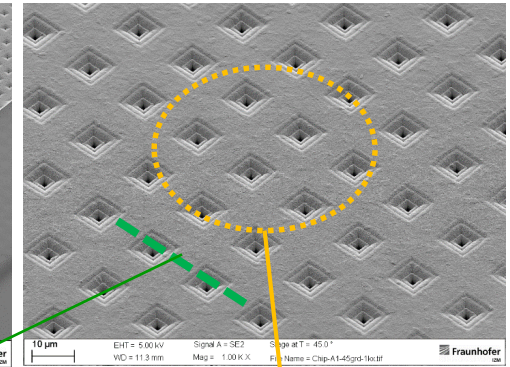
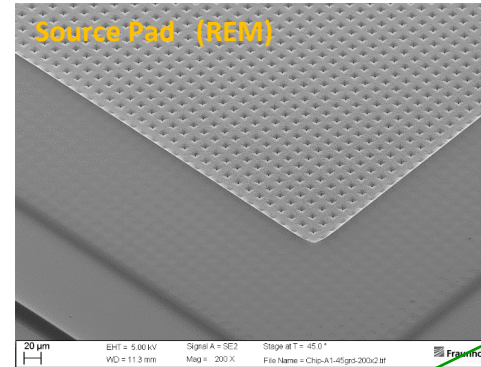


FIB Analyse

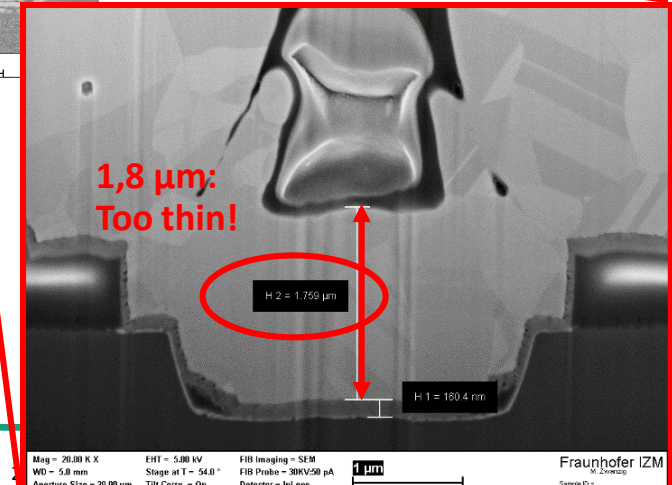
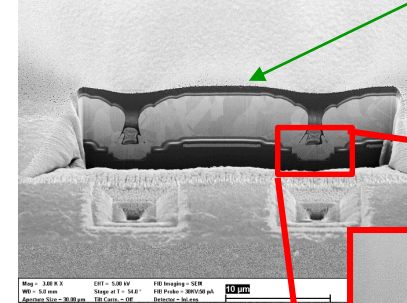


Insufficient

Chip provided by supplier with 5 µm Cu reinforcement



Size of a laser via



Chip Assembly and Sintering - Concept

1. Application of electroless silver on Cu



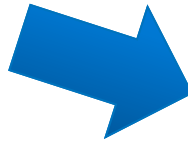
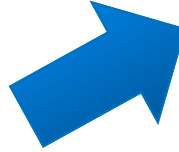
2. Positioning and „pre-sintering“



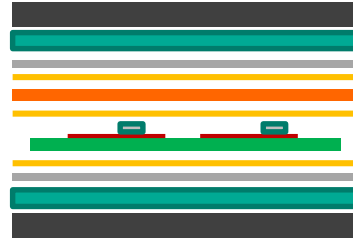
Time for bonding
(due to thick copper core)
30 sek

Temperature:
200 °C

Pressure:
3 kg



3. Press stack build up and sintering



Pressure distribution
Steelplate
Separation foil
Reusable Press Pad
Separation foil
Substrat with chips



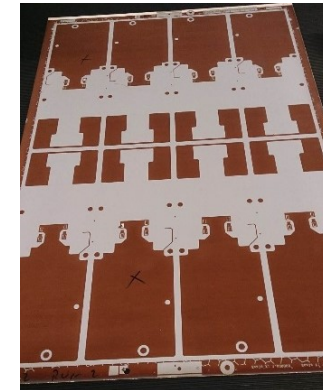
Press time
90 min

Temperatur:
230 °C

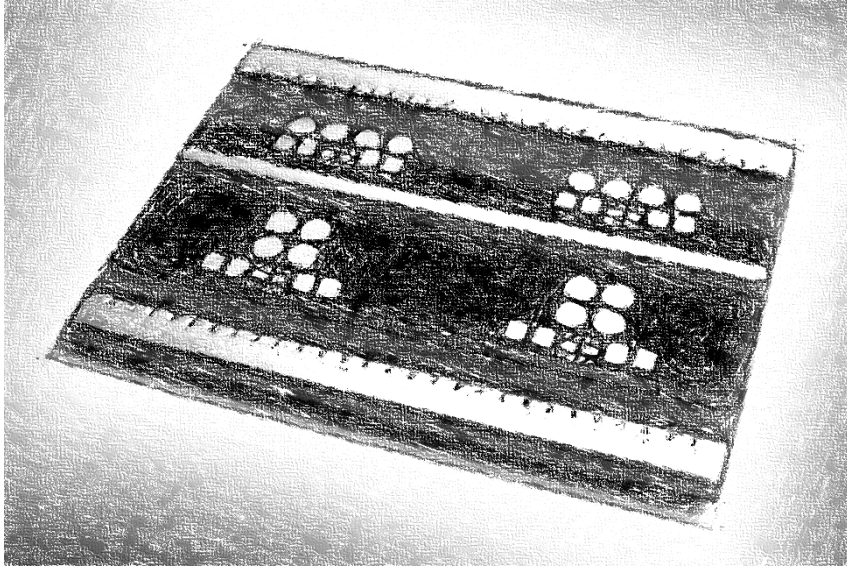
Druck:
30 bar



4. Measurement of x-y position and components height



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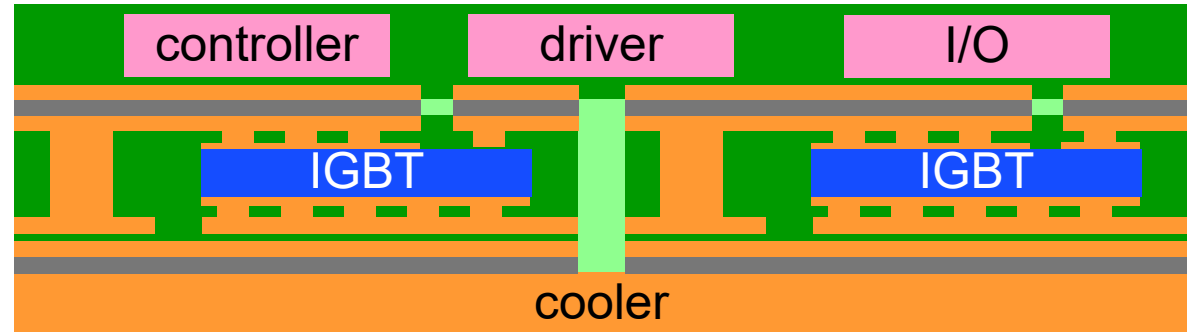
Modular Power Electronics – Concept

- Compact and modular embedded power electronics

- Use established and validated processes for

- Building blocks with integrated logic

- 3D stacks of blocks



- Performance benefits:

- High reliability expected (>300 k power cycles demonstrated)

- Very good heat spreading and vertical heat conductivity

- 3D multiple layer interconnection architecture

- Complete integration of switches and logic into a monolithic block

- Integration capability e. g. direct attachment to electrical engine

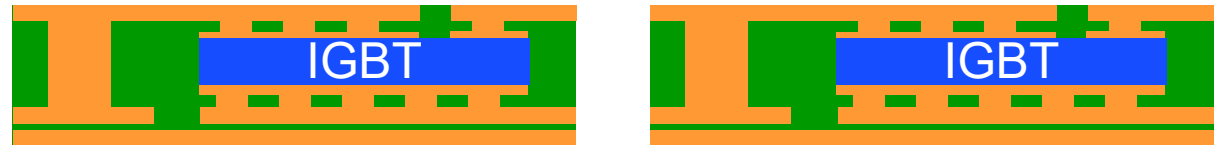
Modular Power Electronics – Requirements

Embedding of **logic** chips



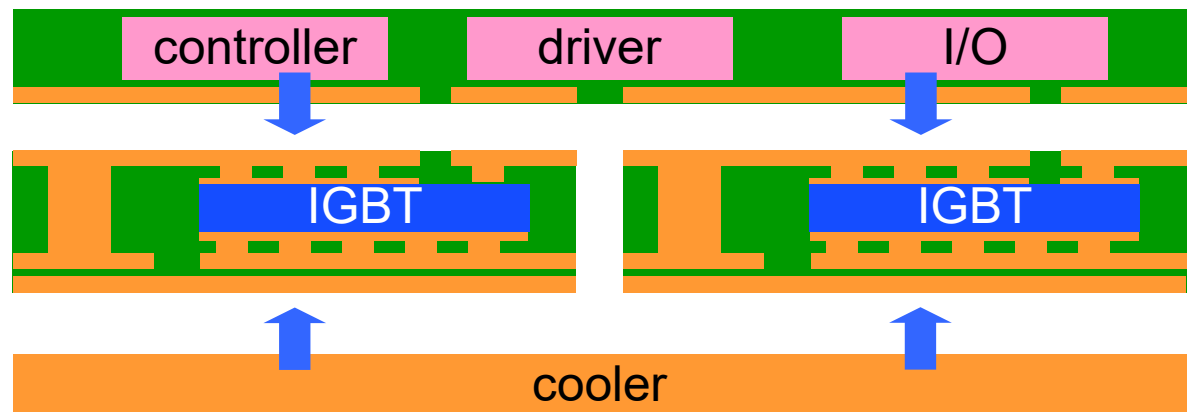
+

Embedding of **power** chips



+

Joining Technology → Sinter/lamination Technology (SLT)



Modularization by Sinter/Lamination

Top IMS

Ag Sinter Paste

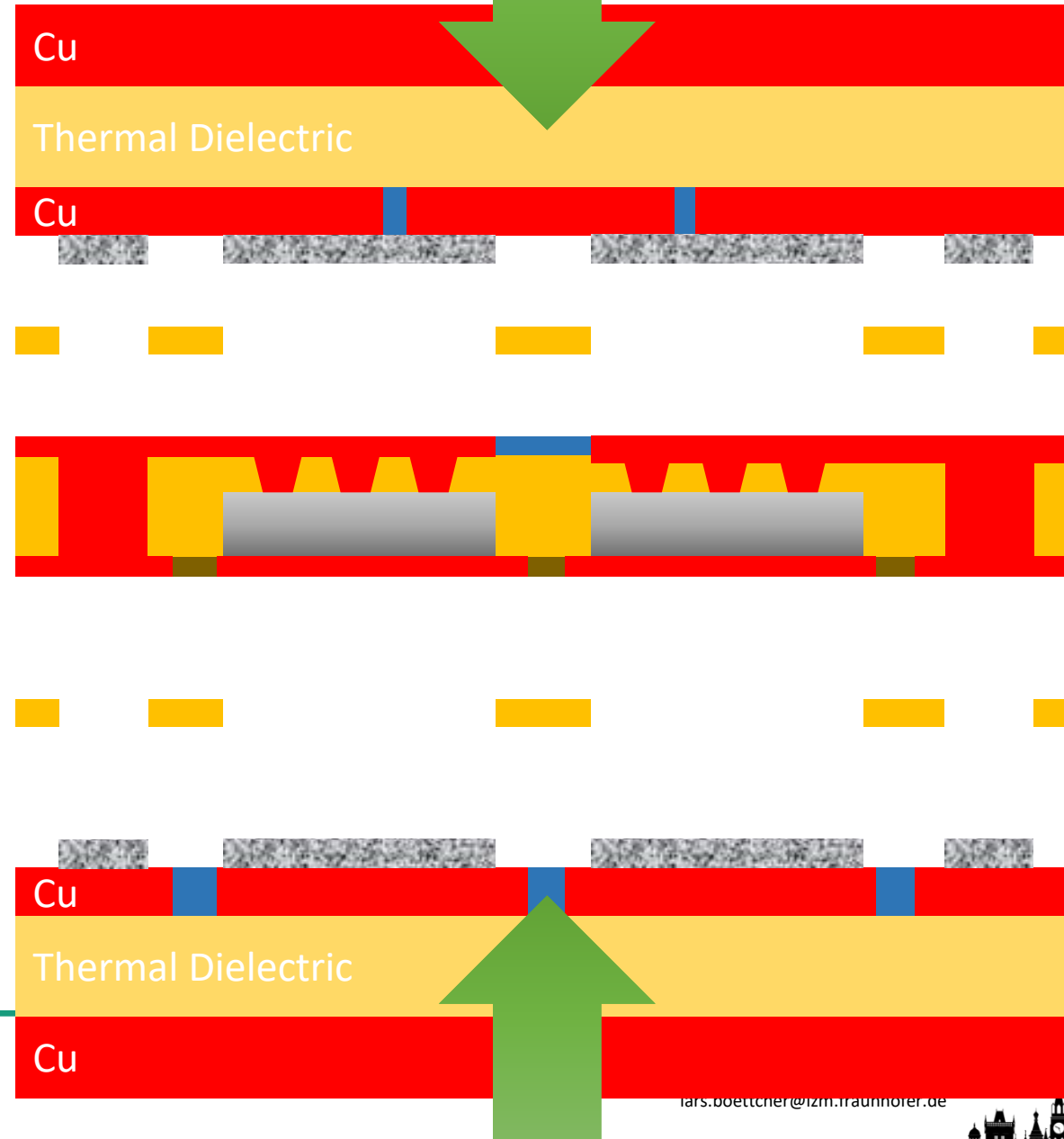
Top Spacer PP

Power Core

Bot Spacer PP

Ag Sinter Paste

Bot IMS



Modularization by Sinter/Lamination

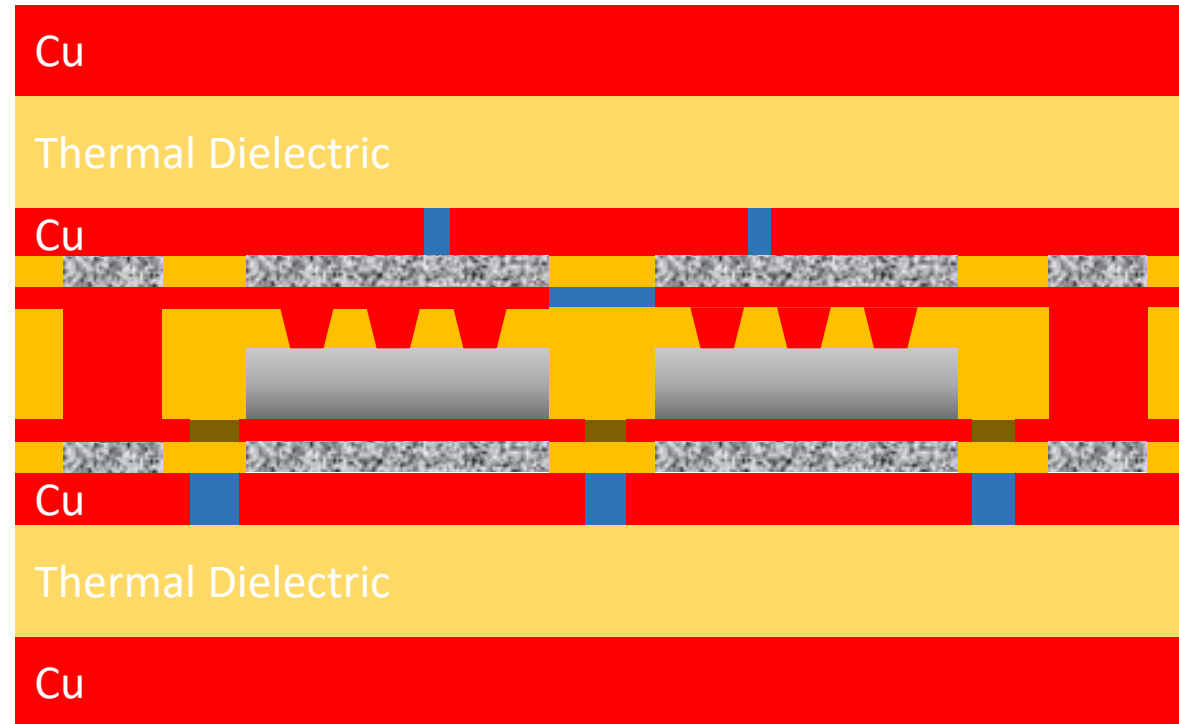
Top IMS

SLT Layer

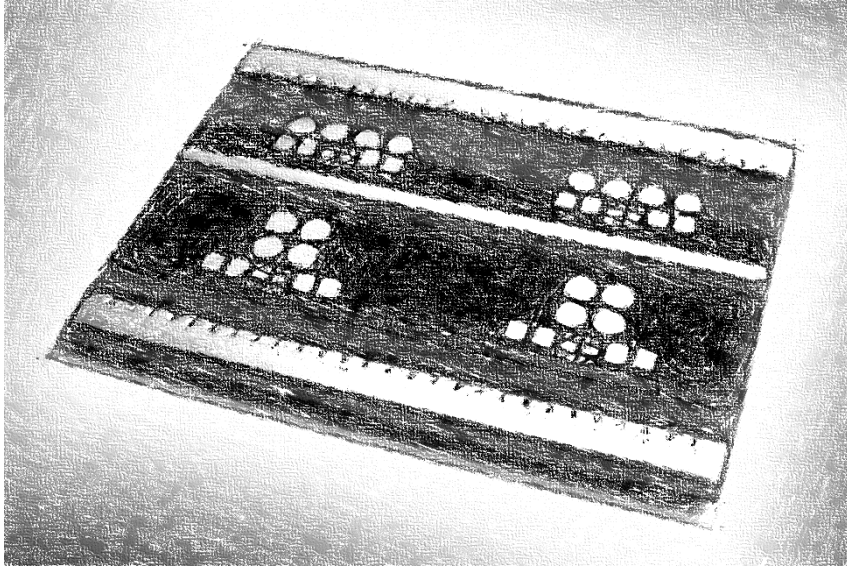
Power Core

SLT Layer

Bot IMS



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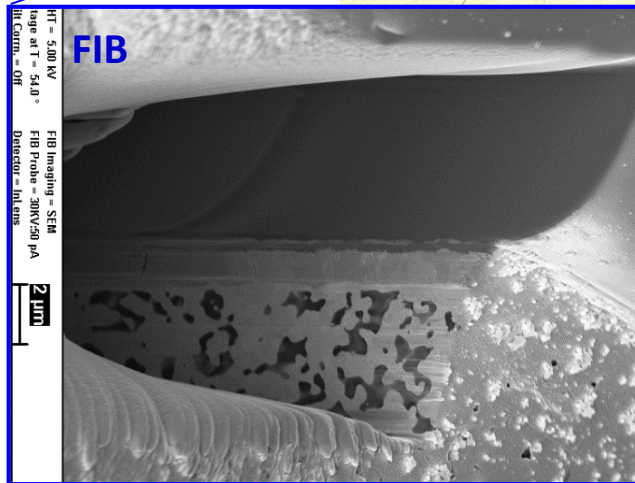
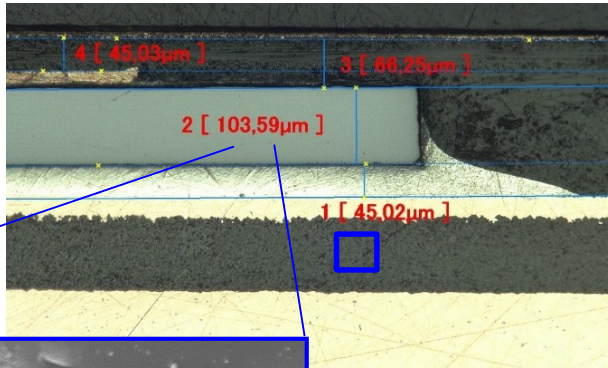
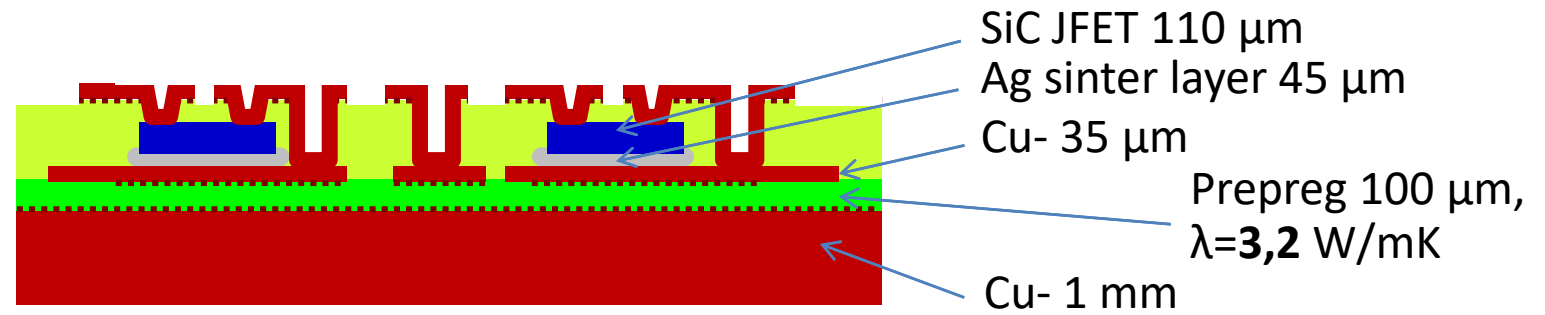
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- Automotive Inverter 50 kW

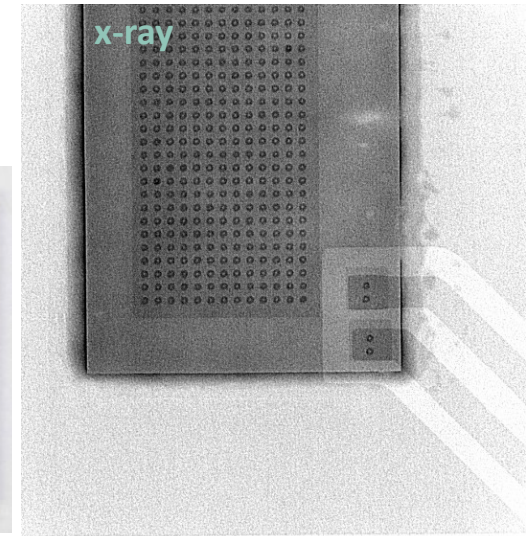
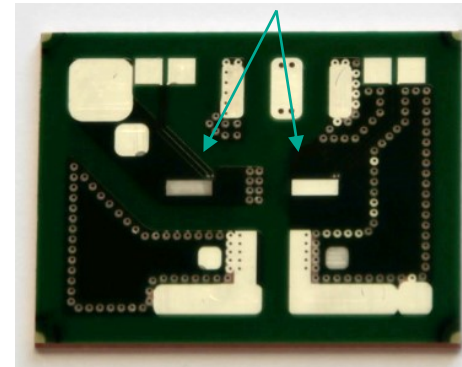
■ Conclusion

■ Acknowledgements

PV inverter – 1.5 to 2.3 kW switching power



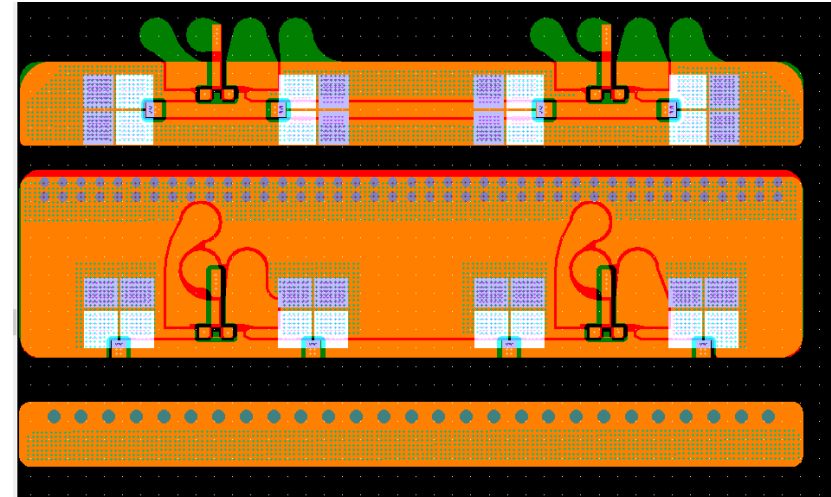
Chip positions



PV inverter – 125kW switching power

■ Module Demonstrator for PV Inverter

- Embedding of SiC on thick copper base (0,5 / 1mm)
- Eight SiC per switch
- Embedding in High Tg Epoxy prepreg
- Two layer build up



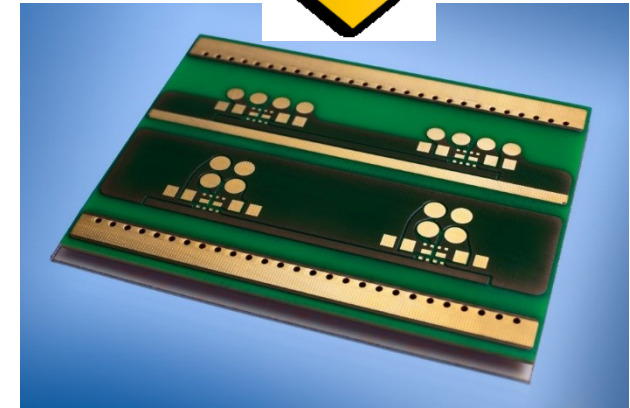
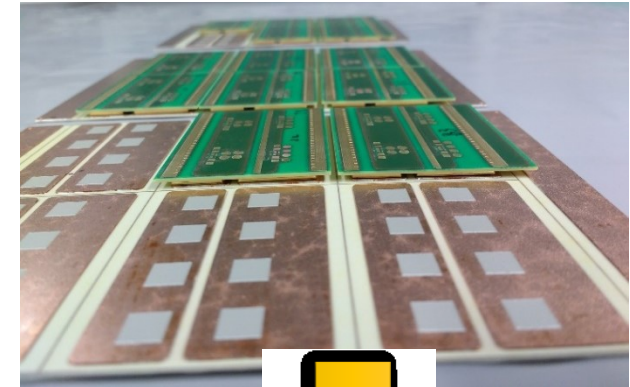
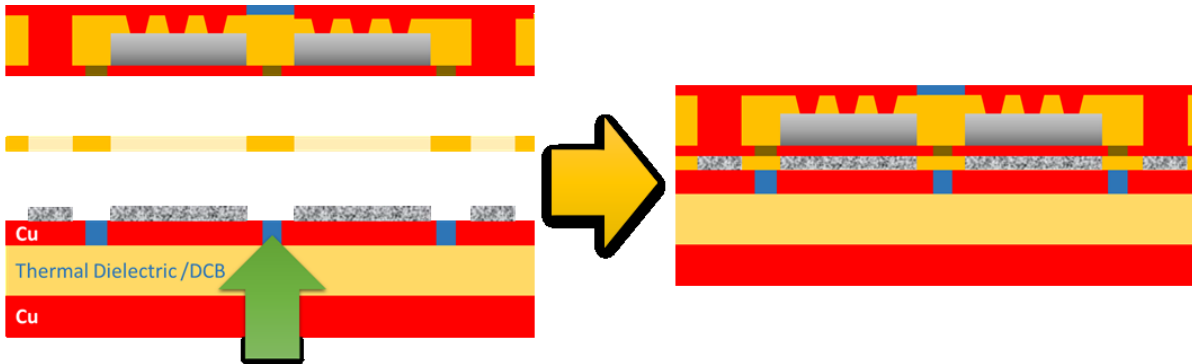
		Top		
SM	Dynamask 5016			35
plated Cu				65
Cu foil	Cu Folie 5/35µm			5
PP	PP GEA 679FGS 1080			80
plated Cu				95
	PP GEA 679FGS 2116			5
Cu foil	Cu Folie 5/35µm			80
PP	PP GEA 679FGS 1080			50
PP	PP GEA 679FGS 1037			120
PP	PP GEA 679FGS 2116			120
PP	PP GEA 679FGS 2116			120
PP	PP GEA 679FGS 2116			500
Cu Substrat				

PP Einlege	4	
Teile		
1		
2		
3		
SiC ca. 380µm		
Ag Sinter ca. 30µm		

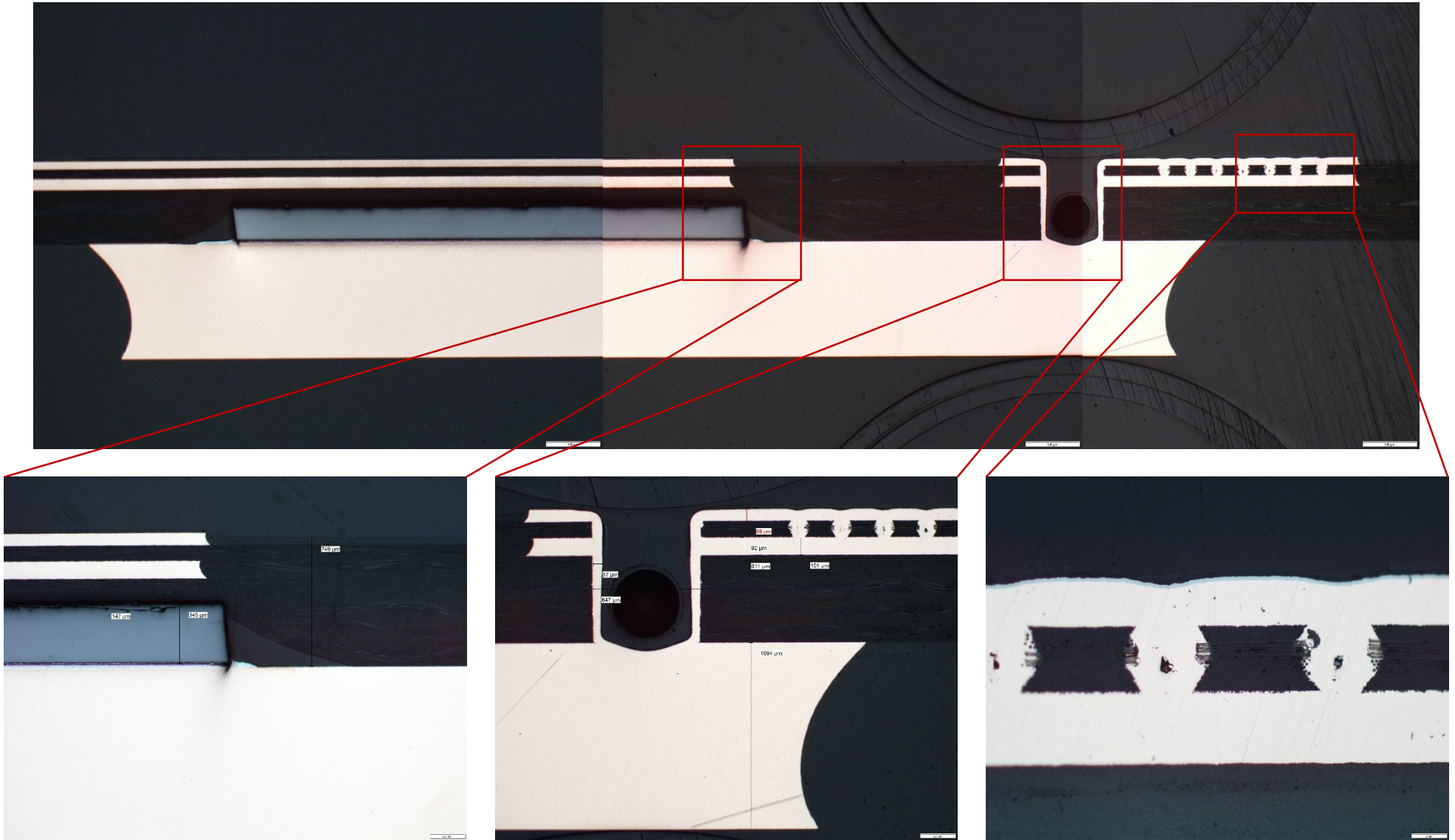
PV inverter – 125kW switching power

■ Thermal management of module

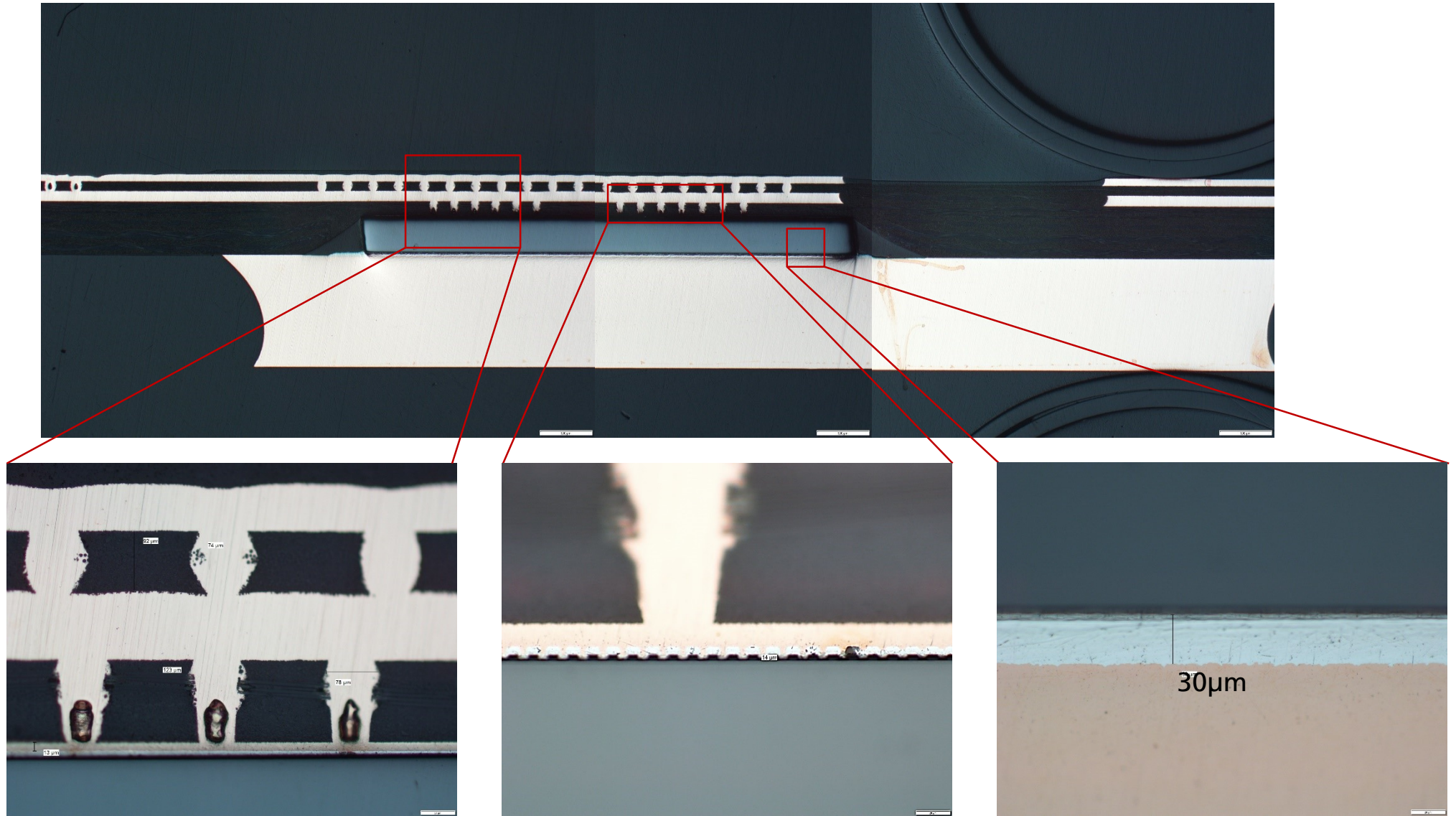
- Ag sintering to ceramic substrate
- Layup and sintering in large panel format
- Possibility to fill air gaps with epoxy resin → sinter-laminate-technology (SLT)



PV inverter – 125kW switching power

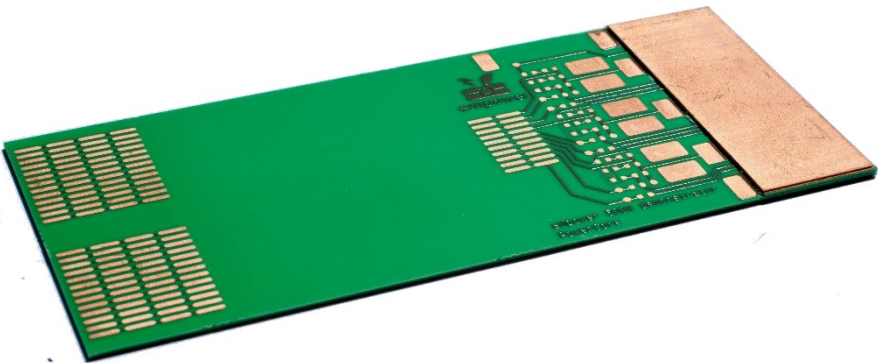
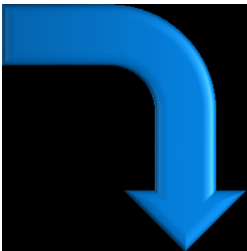
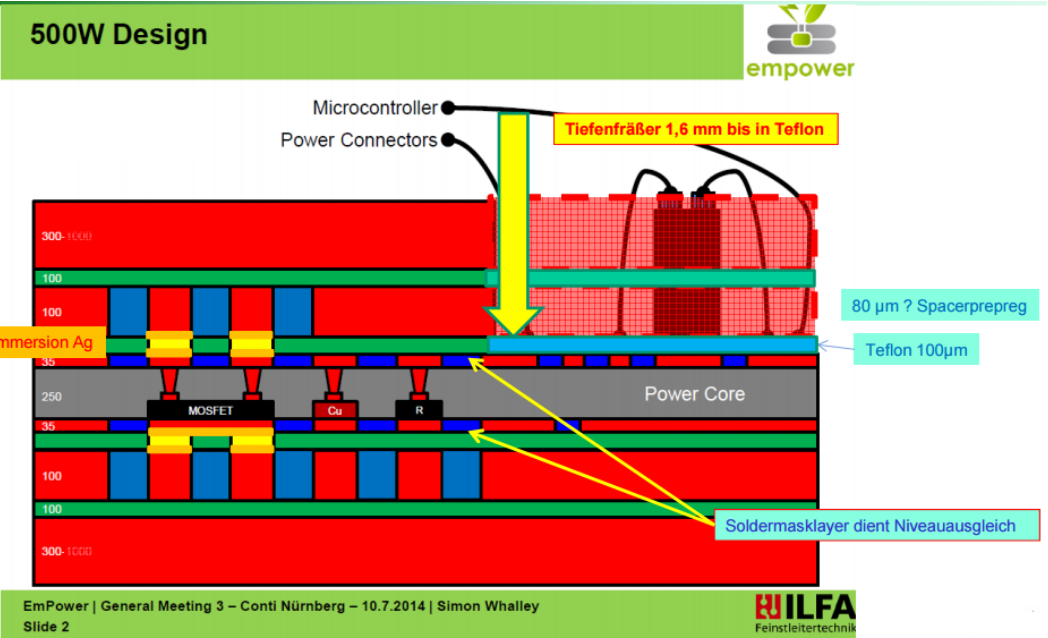


PV inverter – 125kW switching power

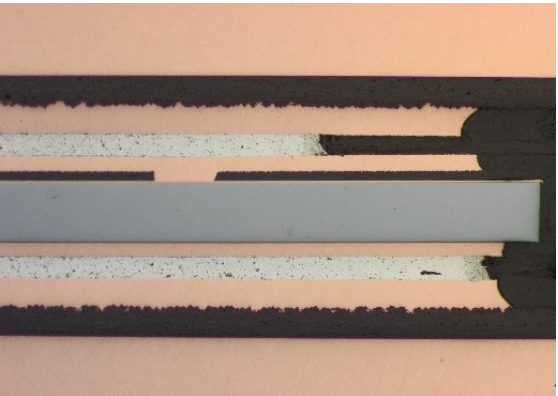
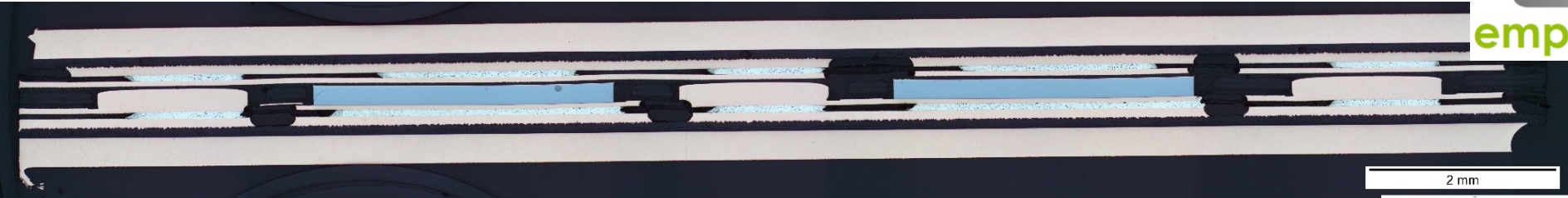




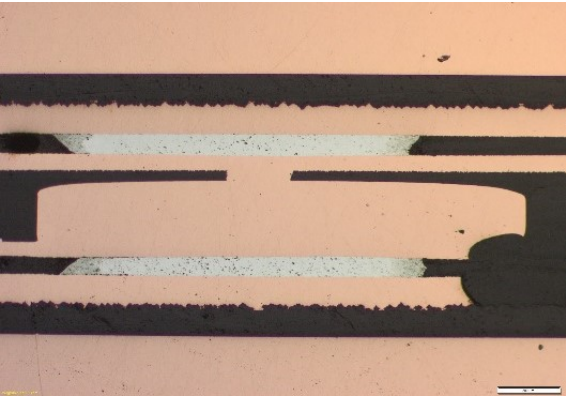
Application – 500W Inverter for Pedelec



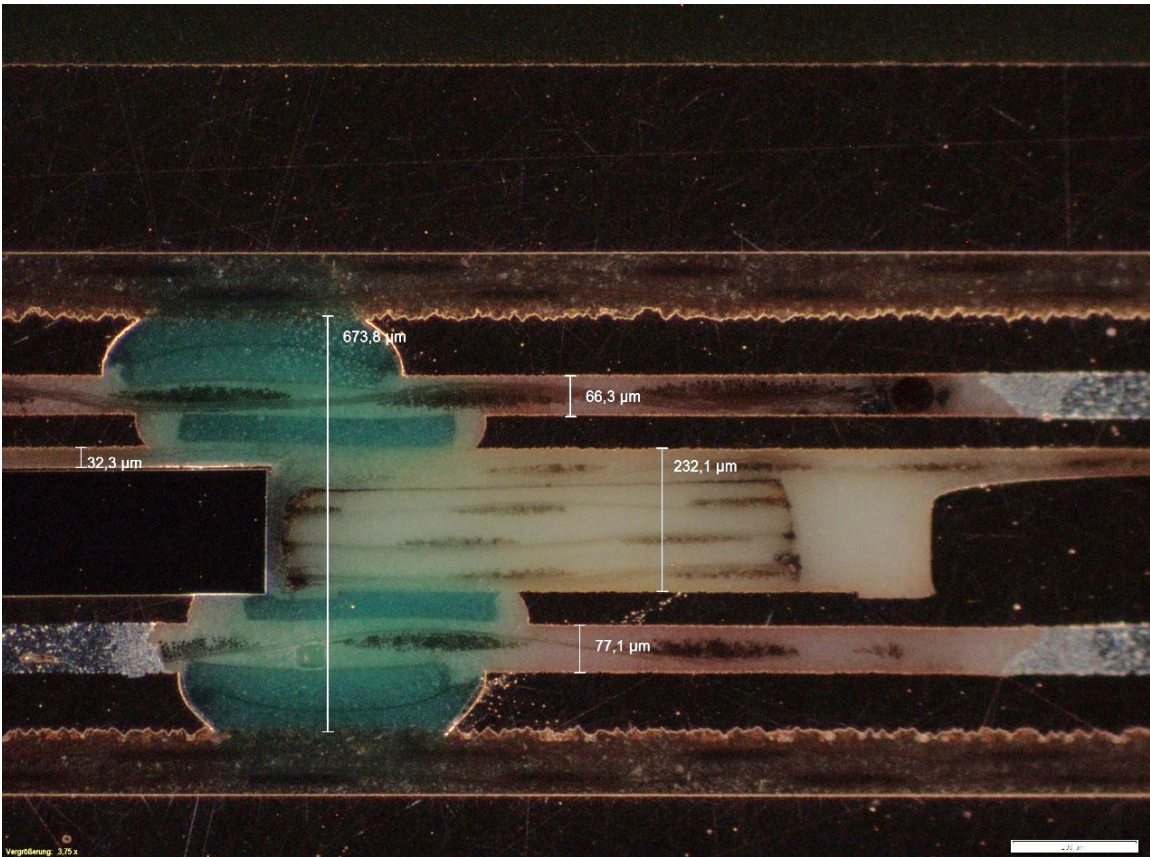
500W Demonstrator Power Core / IMS Sintering

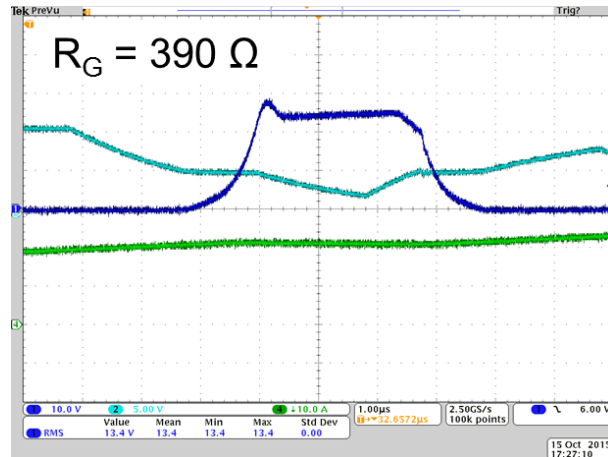


Sinter connection die area



Sinter connection copper inlay area





$R_G = 51 \, \Omega$

V_{DS} V_{GS} I_L

30.15µs	11.80 V
30.05µs	3.000 V
Δ101.0ns	Δ8.000 V

10.0 V 5.00 V 18.8 18.8 10.0 A 100ms 2.50k/s 100k points 1 6.00

Cursors Waveform Screen Source Selected Waveform Bars Horizontal Vertical Linked Off Bring Cursors On Screen Cursor Units

15 Oct 20 14:17:20

Tek PreVu

$R_G = 130\ \Omega$

Trig?

□ 30.6 μs
○ 5.200 V

□ 30.54 μs
○ -1.800 V

△ 92.00 ns
△ 7.690 V

● 10.0 V
● 5.00 V
● +10.0 A

Value	Mean	Min	Max	Std Dev
15.5 V	15.6	15.6	15.6	9.0%

EMF
200ns
44--30.2754μs
2.50kS/s
100k points

Coupling DC AC
Invert On Off
Bandwidth Full
Label Shunt
More

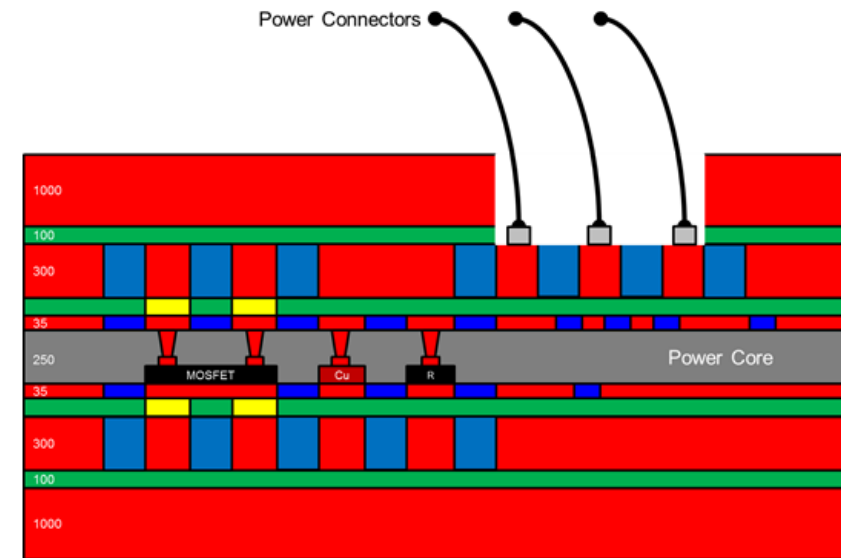
15 Oct 2014
12:27:15

- Switching-behaviour low side MOSFET Embedded module



Application – 50kW inverter for HEV/EV

- Module concept for 50kW Inverter
 - Full area Top and Bottom IMS
 - 1000µm Cu / Thermal dielectric / 300µm Cu
- Power core containing embedded IGBT and Diodes
- Ag Sintering for high current connection and thermal management
- Exposure of high current connector pads by:
 - Depth milling of 1mm Cu
 - Etching of remaining (approx. 100µm) Cu
 - Laser ablation of thermal prepreg at pad positions

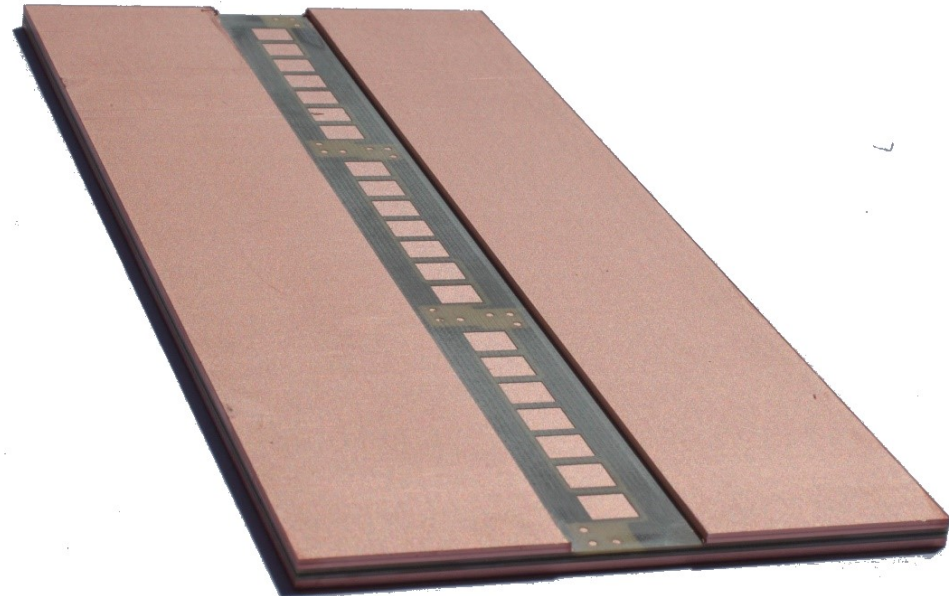


50 kW concept

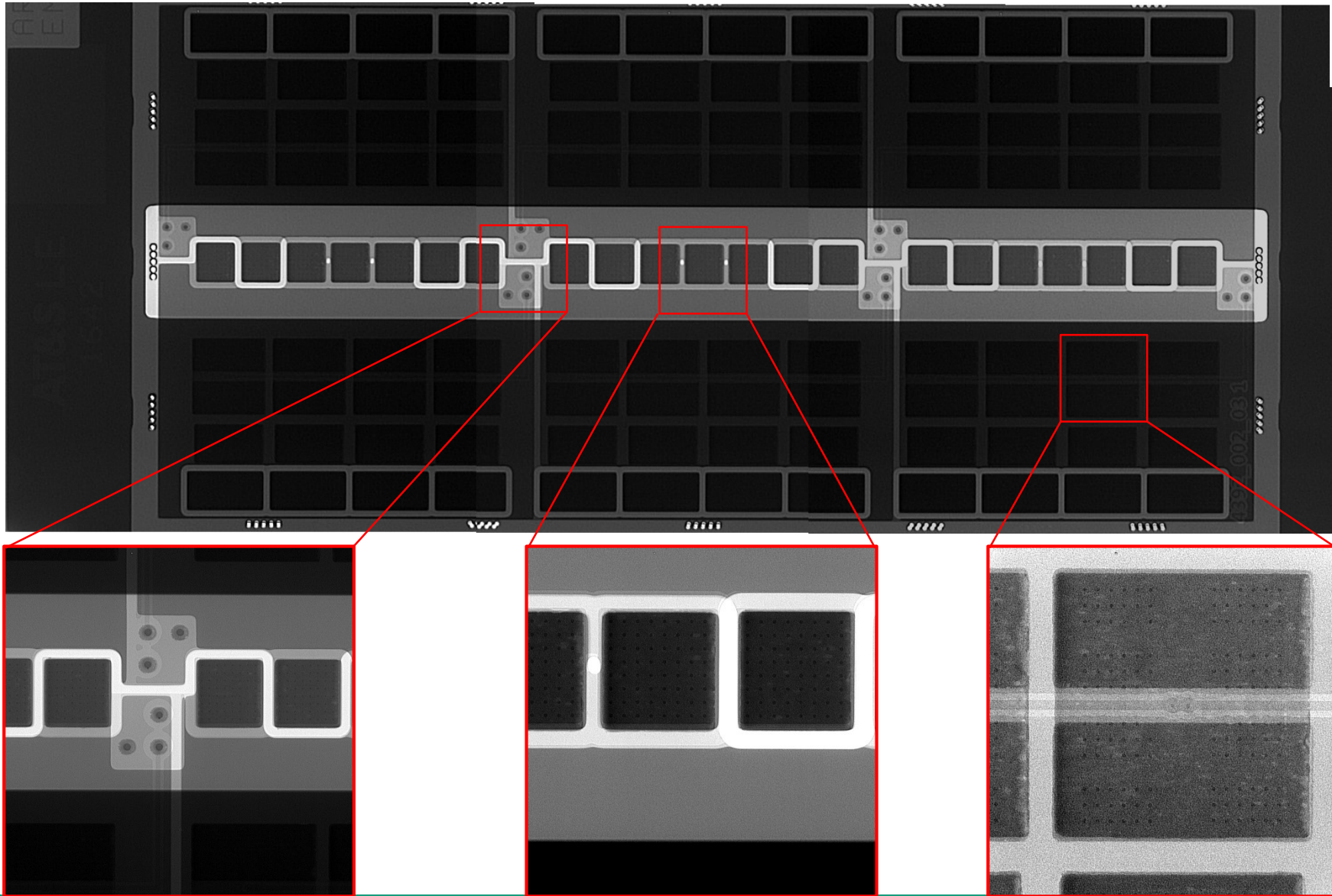


■ Module finalization:

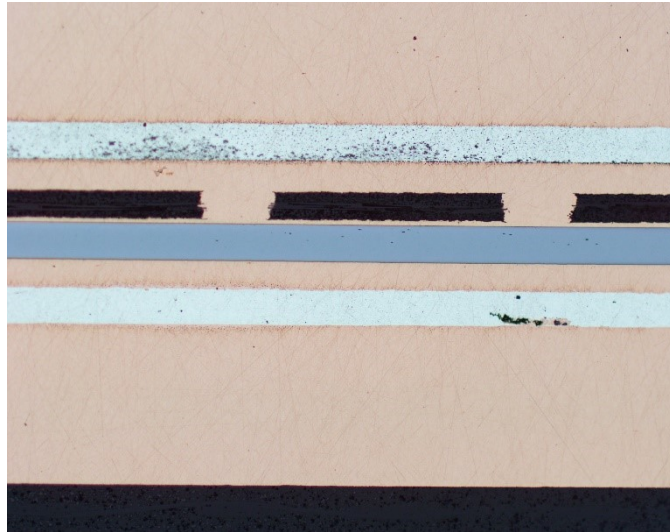
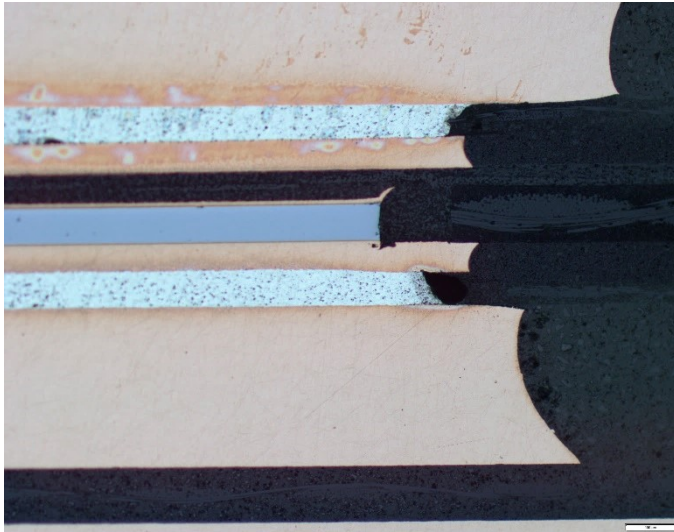
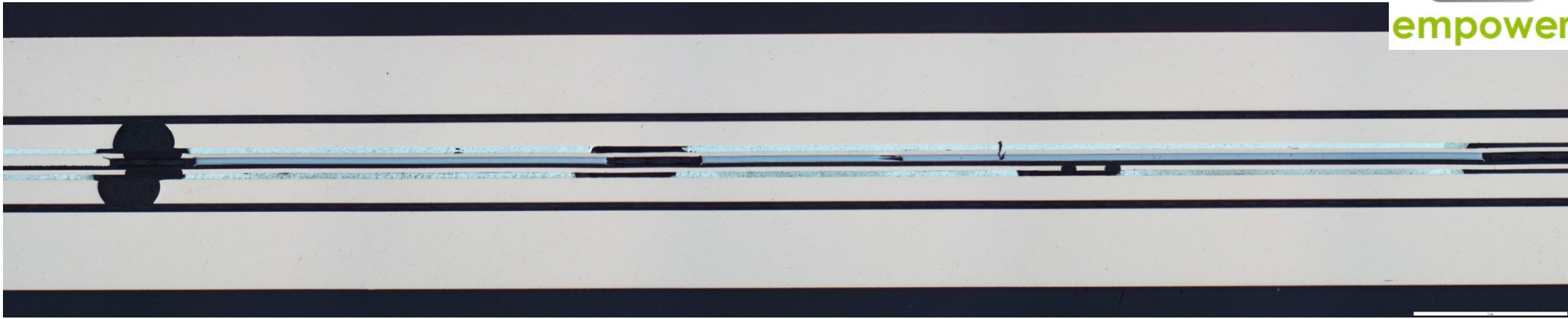
- Depth milling into Top IMS Cu
- Litho & Etch of remaining Cu
- Opening of high current pads on bottom side of top IMS by UV laser ablation



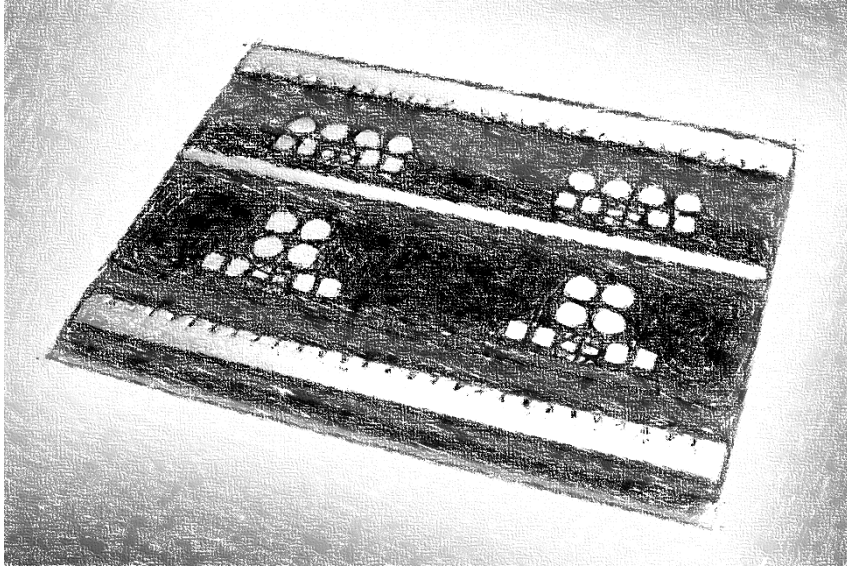
Module Manufacturing – 50kW



Module Manufacturing – 50kW



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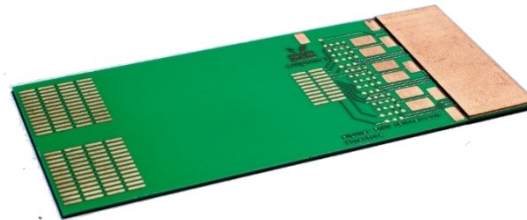
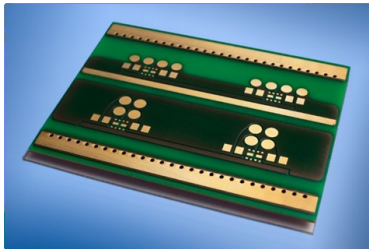
- Photovoltaic Power Inverter 2.3 kW up to 125kW
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- Automotive Inverter 50 kW

■ Conclusion

■ Acknowledgements

Conclusion

- Embedding of power electronic components provides a large benefit in terms of reliability and electrical performance
- Direct copper connection of power dies and short connection length lead to a significant reduction of parasitic inductance. As a result behavior and losses can be reduced.
- Compact and modular embedded power electronics can be realized by use of established processes forming
 - Building blocks with integrated logic
 - 3D stacks of blocks
- Newly development Sinter-Lamination Technology (SLT), is an important technology enabling the modular power electronics concept
- Several application examples in different power classes demonstrate the potential of embedding technologies for power electronic applications



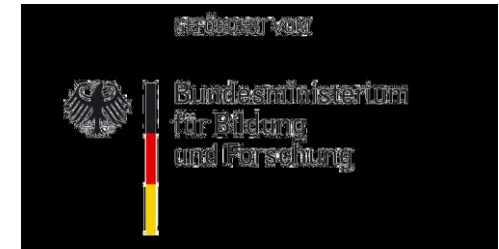
Acknowledgement

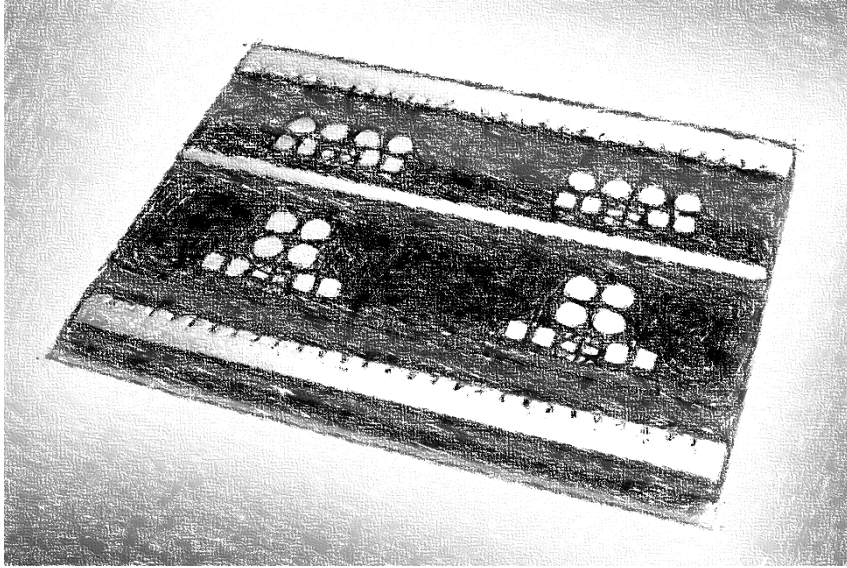
■ German “Federal Ministry of Education and Research” for the financial support of the:

- EmPower project (FKZ 16EKF0016), which is also supported in the European “Catrene” program (CT315 – EmPower)
- LES FLIP project (FKZ 16ES0133)
- LES HHK project (FKZ 16ES0094)

and the partners within the projects

■ Continental for providing the electrical test results for the 500W demonstrator within the EmPower project





Thank you very much for your attention!

Contact: Lars.Boettcher@izm.fraunhofer.de